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Title: SYSTEMS AND METHODS FOR HOLDOVER CIRCUITS IN PHASE LOCKED LOOPS

Commissioner for Patents Washington, D.C. 20231

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materials are

In compliance with 37 C.F.R. §§ 1.56 and 1.97, et seq., the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified Application. Applicant respectfully requests that this Supplemental Information Disclosure Statement be entered and the references listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to MPEP §609, Applicant further requests that the Examiner initial next to each reference on the Form 1449 to indicate that the listed references have been considered. Applicant further requests that a copy of the initialed Form 1449 be returned with the next official communication.

The enclosed references were recently cited in a communication in a counterpart foreign application. A copy of the Communication Relating to the Results of the International Search is enclosed herewith.

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if they are any questions regarding this communication.

CERTIFICATION UNDER 37 CFR 1.97(e)(1)

The undersigned attorney hereby certifies that each item of information contained in this Supplemental Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application as indicated above, not more than three months prior to the mailing date of this Supplemental Information Disclosure Statement.

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If the Examiner has any questions or concerns regarding this application, please contact the undersigned at the number listed below.

Respectfully submitted,

Reg. No. 35,138

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